

L Number	Hits	Search Text	DB	Time stamp
-	30	(714/735.ccls.) and (buffer with test)	USPAT	2003/01/29 18:11
-	10	(714/735.ccls.) and (data adj buffer)	USPAT	2003/01/29 17:50
-	0	(714/735.ccls.) and (data adj buffer) and BIST	USPAT	2003/01/29 17:35
-	0	(714/735.ccls.) and (address adj command adj buffer)	USPAT	2003/01/29 17:51
-	3	(address adj command adj buffer) and BIST	USPAT	2003/01/29 17:52
-	91	(address adj buffer) and (command adj buffer) and test	USPAT	2003/01/29 17:56
-	9	((address adj buffer) with test) and ((command adj buffer) and test)	USPAT	2003/01/29 18:07
-	2	(test adj (address adj buffer))	USPAT	2003/01/29 18:07
-	2	(714/735.ccls.) and (address adj buffer)	USPAT	2003/01/29 18:12
-	2	(714/735.ccls.) and (address adj buffer) and test	USPAT	2003/01/30 09:07
-	9	(address adj command adj buffer) and test	USPAT	2003/01/30 11:41
-	11	(address adj command adj buffer\$1) and test\$3	USPAT	2003/01/30 09:57
-	181	(test with buffer\$1) same address same command	USPAT	2003/01/30 09:58
-	78	((test with buffer\$1) with address) with command	USPAT	2003/01/30 10:17
-	29	test with buffer with memory with address with command	USPAT	2003/01/30 10:47
-	9	test\$3 adj (address adj buffer\$1)	USPAT	2003/01/30 11:29
-	4	test\$3 adj (command adj buffer\$1)	USPAT	2003/01/30 10:52
-	33	test\$3 with (command adj buffer\$1)	USPAT	2003/01/30 11:14
-	94	(address adj command) with data with buffer	USPAT	2003/01/30 11:41
-	21	(address adj command adj buffer) and memory	USPAT	2003/01/30 11:41
-	94	(address adj command) with data with buffer	USPAT	2003/01/30 14:04
-	11	(address adj command adj data) with buffer	USPAT	2003/01/30 14:04
-	308	memory and BIST and interface and controller and compar\$5 and test\$3	USPAT	2003/01/30 16:04
-	2	memory and BIST and interface and controller and compar\$5 and test\$3 and (input adj output adj test)	USPAT	2003/01/30 16:05
-	2	memory and BIST and interface and controller and compar\$5 and test\$3 and (input adj output adj test\$3)	USPAT	2003/01/30 17:40
-	45	controller and (input adj output adj test) and compar\$4	USPAT	2003/01/30 17:42
-	58	controller and (input adj output adj test\$3) and compar\$4	USPAT	2003/01/30 17:48
-	8	controller and ((input adj output adj interface) with test\$3) and compar\$4	USPAT	2003/01/30 17:55
-	29	control\$4 and ((input adj output) with test\$3) and compar\$4 and loopback	USPAT	2003/01/30 17:56
-	3	BIST and memory and (address adj command adj buffer) and (data adj buffer) and test\$3	USPAT	2003/01/31 13:55
-	21	(714/718.ccls.) and BIST and memory and buffer and test\$3	USPAT	2003/01/31 14:44
-	3	(714/718.ccls.) and BIST and memory and (data adj buffer) and test\$3	USPAT	2003/01/31 14:21
-	9	BIST and memory and ((data adj buffer) same compar\$4) and test\$3	USPAT	2003/01/31 15:03
-	176	BIST and memory and (result with register) and test\$3	USPAT	2003/01/31 15:04
-	51	BIST and memory and (result with register with buffer) and test\$3	USPAT	2003/01/31 15:09
-	22	BIST and memory and (test adj result with register) and test\$3	USPAT	2003/01/31 15:09
-	5	(714/718.ccls.) and (clock adj multipl\$3)	USPAT	2003/01/31 16:52
-	12	address adj command adj generator	USPAT	2003/01/31 18:08
-	4	(714/718.ccls.) and (address adj generat\$3) and (command adj generat\$3)	USPAT	2003/01/31 18:09
-	1	(5946247.pn.) and controller	USPAT	2003/02/03 08:54

-	1	(5946247.pn.) and control\$3	USPAT	2003/02/03 08:54
-	1	(5946247.pn.) and control\$4	USPAT	2003/02/03 08:54
-	1	(5946247.pn.) and enabl\$3 and pass\$3 and fail\$3	USPAT	2003/02/03 09:57
-	4	(714/\$.ccls.) and (test adj enabled) and (test adj passed)	USPAT	2003/02/03 11:22
-	12	(test adj enabled) and (test adj passed)	USPAT	2003/02/03 10:05
-	1	(5946247.pn.) and enabled	USPAT	2003/02/03 11:23
-	1	(5946247.pn.) and enabl\$3	USPAT	2003/02/03 11:23
-	1	6182257.pn.	USPAT	2003/02/03 13:13
-	1	5946247.pn.	USPAT	2003/02/03 13:13